

Guidelines for Course Selection for M.S. Students in the Electrical & Computer Engineering Department

Depth Area	Undergraduate Preparation	Core Course Plan A (Thesis)	Core Course(s) Plan B (Project)	Depth Courses (Suggested)
Communication Systems	EE 458: Analog Communication	EE 602: Stochastic Systems	EE 602: Stochastic Systems	EE 558: Digital Communication EE 641: RF Wireless Systems EE 650: Modern Comm Thy EE 652: Prin & Apps of Info Thy EE 653: Coding Theory EE 600: Machine Learning
Computer Networks	EE 410: Signals and Systems	EE 602: Stochastic Systems	EE 602: Stochastic Systems CompE 560: Computer & Data Networks	CompE 560: Computer & Data Networks CompE 565: Multimedia Comm EE 660: High Speed Nets EE 662: Wireless Sensor Nets EE 665: Multimedia Networks EE 600: Machine Learning
Electromagnetic Systems	EE 440: Electromagnetic Waves	EE 602: Stochastic Systems	EE 540: Microwave Design EE 602: Stochastic Systems	EE 534: Solid State Devices EE 540: Microwave Design EE 631: RF Electronic Ckts EE 634: RF Circuit Design EE 641: RF Wireless Systems EE 645: Antennas & Propagation EE 674: Sig. and Power Integrity
Embedded Systems	EE 410: Signals and Systems CompE 475: Microprocessors	EE 602: Stochastic Systems	EE 602: Stochastic Systems	CompE 561: Database & Web Program CompE 570: VLSI System Design CompE 571: Embedded Operating Sys EE 522: Digital Control Systems EE 662: Wireless Sensor Nets EE 600: Machine Learning
Energy Systems and Control	EE 480: Power Systems EE 420: Feedback Control Systems	EE 601: Linear System Theory	EE 601: Linear System Theory	EE 522: Digital Control Systems EE 581: Power Systems Dynamics EE 584: Power Electronics EE 596: Renewable Energy Systems EE 684: Advanced Power Electronics
Signal Processing	EE 410: Signals and Systems	EE 602: Stochastic Systems	EE 601: Linear System Theory EE 602: Stochastic Systems	CompE 565: Multimedia Comm EE 654: Adaptive Algorithms EE 657: Digital Image Proc EE 658: Advanced DSP
VLSI Systems	CompE 470: Digital Circuits	EE 602: Stochastic Systems	EE 602: Stochastic Systems CompE 572: VLSI Ckt Design	CompE 570: VLSI System Design CompE 572: VLSI Ckt Design EE 530: Analog Integ. Ckt Design EE 600: VLSI Testing EE 634: RF Circuit Design EE 670: ASIC Design EE 674: Sig. and Power Integrity

Instructions

These guidelines provide a roadmap for students in either Plan A or Plan B. Before meeting with the ECE Department Graduate Advisor, a classified student should prepare a Program of Study using these guidelines in selecting the courses to be declared for the degree program. The Program of Study should follow the guidelines given below:

General Comments

- Core courses must be taken in the first semester of the program.
- No more than four (4) 500 level courses are accepted for the graduate program.
- Plan A students must take 6 units of EE799 and EE799A under the supervision of a professor as the thesis advisor. The thesis will be taken according to the rules of the Graduate and Research Affairs.
- Plan B students must take EE798 (Project) under the supervision of a professor as the project advisor. The project will be evaluated and approved by two professors including the project advisor in a 30 minute presentation session.
- Students are allowed to take two courses from outside the ECE Department, with the approval of their thesis or project advisor and the Graduate Advisor. If students change thesis or project advisor, these non-ECE courses may not be used as part of their Program of Study.
- Plan B students are encouraged to enroll in the project (EE798) after completing 21 units (as early as the third semester) but they must take it after completing 27 units.
- Plan B students must submit their POS when they take EE798 (Project).
- EE797 and EE799A&B cannot be used in Plan B. If students switch from Plan A to Plan B, they need to remove EE797 and EE799A&B from their POS and take 6 units of regular courses and 3 units of EE798, with the approval of the project advisor and the Graduate Advisor.
- International students can go for CPT after completing 18 units. They need to maintain a GPA of 3.0 or better to qualify for the CPT. They cannot work more than 20 hours per week in a semester in which they have classes and the location of their work must be within the driving distance of SDSU.

Plan A Students: Total of 30 units

- Declare a depth area.
- Take four (4) courses in the depth area including the core course. (**12 units**).
- Take two breadth courses: (**6 units**).
- Take one course either from the depth area or as a breadth course. (**3 units**).
- Register for six units of EE797 (research) and three units of EE799A (thesis): (**9 units**).
- Credit is not given for EE798 for Plan A students.
- Submit the POS to the department signed by the Thesis Advisor.

Plan B Students: Total of 30 units

- Declare a depth area.
- Take six (6) courses in the depth area including the core course(s). (**18 units**).
- Take three breadth courses: (**9 units**).
- Take EE798 (Project) advised by the project advisor. (**3 units**).
- Submit the POS to the department signed by the Project Advisor.

All courses and depth areas listed are subject to the State of California and university funding and therefore may not always be available. Please contact the ECE Department for more information.

List of the Professors in Each Area of Specialization

Communication Systems: Dr. Madhu Gupta, Dr. Santosh Nagaraj, Dr. Duy Nguyen

Computer Networks: Dr. Sunil Kumar, Dr. Yusuf Ozturk, Dr. Christopher Paolini, Dr. Mahasweta Sarkar, Dr. Junfei Xie

Electromagnetic Systems: Dr. Madhu Gupta, Dr. Ege Engin, Dr. Satish Sharma

Embedded Systems: Dr. Baris Aksanli, Dr. Yusuf Ozturk, Dr. Hakan Toreyin, Dr. Junfei Xie

Energy Systems and Control: Dr. Chris Mi, Dr. Reza Sabzehgar, Dr. Saeed Manshadi, Dr. Sridhar Seshagiri, Dr. Lal Tummala

Signal Processing: Dr. Ashkan Ashrafi, Dr. Sunil Kumar

VLSI Systems: Dr. Amir Alimohammad, Dr. Ege Engin, Dr. Ke Huang, Dr. Ying-Khai Teh

Graduate Courses Tentatively Offered in Fall and Spring Semesters

IMPORTANT NOTE: This is just a tentative schedule and is intended to help students plan their Program of Study ahead of time. The courses and the semesters are subject to change without prior notice. **Offering of the courses is also contingent upon the availability of the instructors.**

Fall Semesters		Spring Semesters	
EE502	Electronic Devices Rehab	EE530	Analog Integrated Circuit Design
EE503	Biomedical Instrumentation	EE534	Solid State Devices
EE522	Digital Control Systems	EE540	Microwave Devices and Systems
EE540	Microwave Devices and Systems	EE581	Power System Dynamics
EE558	Digital Communications	EE600	VLSI Testing
EE584	Power Electronics	EE600	Digital ASIC Design
EE596	Renewable Energy Smart Grid	EE602	Stochastic Signals & Systems
EE601	Linear Sys Theory & Design	EE634	RF Circuit Design
EE602	Stochastic Signals & Systems	EE641	RF Wireless Systems
EE631	RF Electronics Circuits	EE650	Modern Communication Theory I
EE645	Antennas and Propagation	EE652	Principles & App of Information Theory
EE654	Adaptive Algorithms	EE653	Coding Theory
EE657	Digital Image Processing	EE658	Advanced Digital Signal Process
EE662	Wireless Sensor Networks	EE660	High Speed Net Design
EE674	Signal and Power Integrity	EE665	Multimedia Wireless Networks
COMPE560	Computer and Data Networks	EE684	Advanced Power Electronics
COMPE565	Multimedia Communication Systems	EE685	MEMS Design and Applications
COMPE570	VLSI System Design	COMPE560	Computer and Data Networks
COMPE571	Embedded Operating System	COMPE561	Database and Web Programming
COMPE572	VLSI Circuit Design	COMPE565	Multimedia Communication Systems